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PATENT
Attorney Docket No.: SLM-08400

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	Group Art Unit: 3662
Kais Almarzouk et al.)	Examiner:
Serial No.: 09/934,050)	<u>TRANSMITTAL LETTER</u>
Filed: August 21, 2001)	162 N. Wolfe Road
For: METHOD AND APPARATUS FOR)	Sunnyvale, CA 94086
MEASURING WAVELENGTH)	(408) 530-9700
JITTER OF LIGHT SIGNAL)	Customer No.: 28960

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed is a Revocation of Prior Powers of Attorney and New Power of Attorney, including Exhibit A for filing with the U.S. Patent and Trademark Office.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: 4-21-04

By: Thomas B. Haverstock
Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicants

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450

HAVERSTOCK & OWENS LLP.
Date: 4/21/04 By: [Signature]



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

See attached table of Exhibit A

Serial No.:

Filing Date:

For:

**POWER OF ATTORNEY AND
REVOCATION OF PRIOR
POWERS OF ATTORNEY**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Silicon Light Machines, owner of record of the applications and patents included within the table of Exhibit A attached hereto, hereby revokes all prior powers of attorney which may have been filed with these applications and patents and appoints Patrick D. Benedicto, Registration Number 40,909, of the firm of Okamoto & Benedicto, LLP, as its attorney with full power of substitution to transact all business in the U.S. Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding these applications and patents to:

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Dated:

4/26/04

By:

Charles B. Roxlo

Name:

C. B. ROXLO

Title:

VP ENGR

EXHIBIT A

OUR FILE #	SERIAL NO.	FILING DATE	INVENTOR(S)/ APPLICANT	TITLE	PATENT NO.	ISSUED DATE
00500	08/482,188	6/7/95	Dave Corbin et al.	FLAT DIFFRACTION GRATING LIGHT VALVE	5,841,579	11/24/98
00600	08/482,192	6/7/95	David Bloom	CLEAR-BEHIND MATRIX ADDRESSING FOR DISPLAY SYSTEMS	5,798,743	8/25/98
00700	08/480,459	6/7/95	Jonathan Bornstein et al.	A METHOD OF MAKING AND AN APPARATUS FOR A FLAT DIFFRACTION GRATING LIGHT VALVE	5,661,592	8/26/97
00800	08/473,759	6/7/95	Bryan Staker et al.	DIFFRACTION GRATING LIGHT DOUBLING COLLECTION SYSTEM	5,629,801	5/13/97
01000	09/103,353	6/23/98	Dave Bloom	EMBOSSED DIFFRACTION GRATING ALONE AN IN COMBINATION WITH A CHANGEABLE IMAGE DISPLAY	6,101,036	8/8/00
01200	08/638,605	8/26/96	David Bloom et al.	METHOD AND APPARATUS FOR MODULATING A LIGHT BEAM	5,808,797	9/15/98
01300	08/763,523	11/16/99	Dave Corbin et al.	DISPLAY/MONITOR WITH ORIENTATION DEPENDENT ROTATABLE IMAGE	5,986,634	11/16/99
01500	08/635,479	4/22/96	Richard J. E. Aras et al.	TIME-INTERLEAVED BITPLANE, PULSE- WIDTH-MODULATION DIGITAL DISPLAY SYSTEM	5,731,802	3/24/98
01600	08/744,099	11/5/96	Richard J.E. Aras et al.	BANDWIDTH AND FRAME BUFFER SIZE REDUCTION IN A DIGITAL PULSE-WIDTH- MODULATED DISPLAY SYSTEM	6,064,404	5/16/00
01800.	09/124,710	7/29/98	James Gill Shook	METHOD OF AND APPARATUS FOR SEALING A HERMETIC LID TO A SEMICONDUCTOR DIE	6,303,986 B1	10/16/01
01803	09/866,266	5/24/01	James Gill Shook	METHOD OF AND APPARATUS FOR SEALING A HERMETIC LID TO A SEMICONDUCTOR DIE		

OUR FILE #	SERIAL NO.	FILING DATE	INVENTOR(S)/ APPLICANT	TITLE	PATENT NO.	ISSUED DATE
01900	08/920,122	9/2/97	Dave Corbin et al.	ELECTRICAL INTERFACE TO INTEGRATED CIRCUIT DEVICE HAVING HIGH DENSITY I/O COUNT	6,096,576	8/1/00
01907	09/500,118	2/8/00	Dave Corbin et al.	ELECTRICAL INTERFACE TO INTEGRATED CIRCUIT DEVICE HAVING HIGH DENSITY I/O COUNT	6,452,260 B1	9/17/02
02400	09/104,159	6/24/95	David Bloom et al.	METHOD AND APPARATUS FOR MODULATING AN INCIDENT LIGHT BEAM FOR FORMING A TWO- DIMENSIONAL IMAGE	6,215,579	4/10/01
02500	09/092,220	6/25/98	Chris Gudeman	VAPOR PHASE LOW MOLECULAR WEIGHT LUBRICANTS	6,004,912	12/21/99
02502	09/467,083	12/20/99	Chris Gudeman	METHOD AND APPARATUS FOR APPLYING VAPOR PHASE LUBRICANT TO DEVICE	6,251,842 B1	6/26/01
02600	09/092,245	6/5/98	Dave Corbin	STEREO HEAD MOUNTED DISPLAY USING A SINGLE DISPLAY DEVICE	6,271,808	8/7/01
02901	08/821,390	3/20/97	David Bloom et al.	DISPLAY DEVICE INCORPORATING ONE DIMENSIONAL HIGH-SPEED GRATING LIGHT VALVE ARRAY	5,982,553	11/9/99
03001	08/821,394	3/20/97	David Bloom et al.	DISPLAY SYSTEM INCLUDING AN IMAGE GENERATOR AND MOVEABLE SCANNER FOR SAME	5,764,280	6/9/98
03700	08/961,826	10/31/97	Paul K. Manhart	DISPLAY APPARATUS INCLUDING GRATING LIGHT-VALVE ARRAY AND INTERFEROMETRIC OPTICAL SYSTEM	6,088,102	7/11/00
03800	10/187,159	6/28/02	Jim Hunter et al.	REDUCED FORMATION OF ASPERITIES IN CONTACT MICRO-STRUCTURES		
03800WO	PCT/US03/15 213	5/14/03	Silicon Light Machines	REDUCED FORMATION OF ASPERITIES IN CONTACT MICRO-STRUCTURES		
03900	09/802,619	3/8/01	Aki Tomita et al.	HIGH CONTRAST GRATING LIGHT VALVE		
03900WO	PCT/US02/06 715	2/28/02	Silicon Light Machines	HIGH CONTRAST GRATING LIGHT VALVE		

OUR FILE #	SERIAL NO.	FILING DATE	INVENTOR(S)/ APPLICANT	TITLE	PATENT NO.	ISSUED DATE
04000	09/354,643	3/6/01	Greg Miller	METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE BARS	6,356,577 B1	3/12/02
04100	10/161,193	5/28/02	Dave Corbin et al.	OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION		
04100WO	PCT/US03/15 543	5/14/03	Silicon Light Machines	OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION		
04300	09/498,703	2/7/00	Jahja Trisnadi	SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING		
04400	09/921,266	8/1/01	Chris Gudeman et al.	GRATING LIGHT VALVE WITH ENCAPSULATED DAMPENING GAS	6,646,778 B2	11/11/03
05101	10/237,239	9/5/02	Kenneth P. Gross et al.	DISPLAY APPARATUS RGB COLOR COMBINER AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEN FILTER	6,692,129 B2	2/17/04
05101WO	PCT/US02/37 573	11/21/02	Silicon Light Machines	DISPLAY APPARATUS RGB COLOR COMBINER AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEN FILTER		
05200	10/161,191	5/28/02	Jim Hunter	INTEGRATED DRIVER PROCESS FLOW		
05200WO	PCT/US03/15 475	5/14/03	Silicon Light Machines	INTEGRATED DRIVER PROCESS FLOW		
05300	09/687,465	10/11/00	Jahja Trisnadi	METHOD AND APPARATUS FOR REDUCING LASER SPECKLE	6,323,984 B1	11/27/01
05700	09/461,503	12/14/99	Greg Miller et al.	LASER BEAM ATTENUATOR AND METHOD OF ATTENUATING A LASER BEAM	6,497,490 B1	12/24/02
05800	09/832,738	4/10/01	Rob Corrigan et al.	METHOD, SYSTEM, AND DISPLAY APPARATUS FOR ELECTRONIC CINEMA		
05900	09/664,020	9/19/00	Christopher Gudeman	METHOD AND APPARATUS FOR INTERFEROMETRIC MODULATION OF LIGHT	6,466,354 B1	10/15/02

OUR FILE #	SERIAL NO.	FILING DATE	INVENTOR(S)/ APPLICANT	TITLE	PATENT NO.	ISSUED DATE
06100	10/047,5501	1/15/02	Gregory Miller	METHOD FOR LITHOGRAPHY-BASED DOMAIN PATTERNING OF LOW-COERCIVE-FIELD FERRO ELECTRICS		
06300	09/574,733	5/18/00	Rob Corrigan	PROJECTOR WITH SEPARATE LIGHT SOURCE AND PROJECTION OPTICS	6,480,634 B1	11/12/02
06400	09/930,838	8/15/01	David Amm et al.	BLAZED GRATING LIGHT MODULATOR		
06401	10/321,342	12/16/02	David Amm et al.	BLAZED GRATING LIGHT MODULATOR		
06500	09/930,820	8/15/01	David Amm	STRESS TUNED BLAZED GRATING LIGHT VALVE	6,639,722 B2	10/28/03
06700	10/187,137	6/28/02	David Amm	A METHOD AND DEVICE FOR MODULATING A LIGHT BEAM AND HAVING AN IMPROVED RESPONSE	6,714,337 B1	3/30/04
06700WO	PCT/US03/15 348	5/14/03	Silicon Light Machines	A METHOD AND DEVICE FOR MODULATING A LIGHT BEAM AND HAVING AN IMPROVED RESPONSE		
07000	09/898,882	7/2/01	Jahja Trisnadi	HADAMARD PHASE PATTERNS FOR OPTIMUM LASER SPECKLE REDUCTION		
07000WO	PCT/US02/20 053	6/24/02	Silicon Light Machines	METHOD, APPARATUS AND DIFFUSER FOR REDUCING LASER SPECKLE (as amended in the ISR)		
07600WO	PCT/US02/09 604	3/27/02	Silicon Light Machines	ANGLED ILLUMINATION FOR SINGLE ORDER, GLV BASED PROJECTION SYSTEM		
07601	09/930,831	8/15/01	David Thomas Amm	ANGLED ILLUMINATION FOR SINGLE ORDER, GLV BASED PROJECTION SYSTEM	6,707,591 B2	3/16/04
07700	09/832,699	4/10/01	David Thomas Amm	MODULATION FOR LIGHT OUT OF THE FOCAL PLANE, IN A GLV BASED PROJECTIONS SYSTEM	6,614,580 B2	9/2/03
07700WO	PCT/US02/09 602	3/27/02	Silicon Light Machines	MODULATION FOR LIGHT OUT OF THE FOCAL PLANE, IN A GLV BASED PROJECTIONS SYSTEM		

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07900	09/782,387	2/12/01	Clinton Carlisle et al.	AN ILLUMINATION SYSTEM FOR ONE-DIMENSIONAL SPATIAL LIGHT MODULATORS EMPLOYING MULTIPLE LIGHT SOURCES	6,567,584 B2	5/20/03
08000	10/051,972	1/15/02	Clinton Carlisle et al.	METHOD AND APPARATUS FOR DYNAMIC EQUALIZATION IN WAVELENGTH DIVISION MULTIPLEXING		
08100	09/765,922	1/19/01	Alexander Payne et al.	REDUCED SURFACE CHARGING IN SILICON-BASED DEVICES	6,387,723 B1	5/14/02
08101	10/100,627	3/14/02	Alexander Payne et al.	REDUCED SURFACE CHARGING IN SILICON-BASED DEVICES	6,660,552 B2	12/9/03
08200	09/875,499	6/5/01	Gregory Miller et al.	FIBER OPTIC TRANSCEIVER		
08300	09/952,626	9/13/01	Michael Bruner	MICROELECTRONIC MECHANICAL SYSTEM AND METHODS		
08300WO	PCT/US02/27822	8/29/02	Silicon Light Machines	MICROELECTRONIC MECHANICAL SYSTEM AND METHODS		
08301	10/268,257	10/9/02	Michael Bruner	MICROELECTRONIC MECHANICAL SYSTEM AND METHODS		
08400	09/934,050	8/21/01	Dinesh Maheshwari et al.	METHOD AND APPARATUS FOR MEASURING WAVELENGTH JITTER OF LIGHT SIGNAL		
08500	10/377,977	2/28/03	Clinton Carlisle et al.	HIGH SENSITIVITY, HIGH RESOLUTION SPECTRAL CHANNEL MONITOR		
08600	09/931,674	8/16/01	Omar Leung	ENHANCE THERMAL STABILITY THROUGH OPTICAL SEGMENTATION	6,587,253 B2	7/1/03
08700	10/231,429	8/28/02	Syed Tariq Shafaat et al.	OPTICAL COUPLER		
08800	10/291,264	11/8/02	Syed Tariq Shafaat et al.	METHOD OF MANUFACTURING OPTICAL COUPLER		

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08900	10/232,790	8/28/02	Syed Tariq Shafaat et al.	OPTICAL COMMUNICATION ARRANGEMENT		
09000	10/231,346	8/28/02	Syed Tariq Shafaat et al.	OPTICAL COUPLER INCLUDING ALIGNMENT FEATURE		
09100	10/160,305	5/29/02	Josef Berger et al.	OPTICAL SWITCH		
09500	10/183,585	6/26/02	Clinton Carlisle et al.	RAPIDLY TUNABLE EXTERNAL CAVITY LASER		
09600	10/225,211	8/20/02	Alexander Payne et al.	2-D DIFFRACTION GRATING FOR SUBSTANTIALLY ELIMINATING POLARIZATION DEPENDENT LOSSES		
09700	10/323,560	12/18/02	Alexander Payne et al.	FABRY-PEROT INTERFEROMETER INCLUDING MEMBRANE SUPPORTED REFLECTOR		
09900	10/367,440	2/14/03	Wilhelmus de Groot et al.	METHOD AND APPARATUS FOR LEVELING THERMAL STRESS IN MULTI-LAYER MEMS DEVICES		
10000	10/256,558	9/27/02	Omar Leung et al.	CONTROLLED CURVATURE OF STRESSED MICRO-STRUCTURES	6,712,480 B1	3/30/04
10100	10/112,962	3/28/02	Mike Bruner et al.	MICROELECTRONIC MECHANICAL SYSTEM AND METHODS		
10200	10/179,664	6/24/02	Omar Leung	METHOD TO CREATE A HERMETIC PACKAGE WITH TILTED COVER		
10300	10/225,370	8/20/02	Jim Hunter et al.	GRATING LIGHT VALVE WITH INDIVIDUALLY ADDRESSABLE RULINGS		
10400	10/159,715	5/31/02	Clinton Carlisle et al.	LIGHT MODULATOR STRUCTURE FOR PRODUCING HIGH-CONTRAST OPERATION USING ZERO-ORDER LIGHT (as amended)		
10500	10/318,658	12/12/02	Jahja Trisnadi et al.	APPARATUS FOR SELECTIVELY BLOCKING WDM CHANNELS		
10600	10/327,357	12/20/02	Clinton Carlisle et al.	ARRANGEMENT FOR WDM MULTIPLEXING AND DEMULTIPLEXING		

OUR FILE #	SERIAL NO.	FILING DATE	INVENTOR(S)/ APPLICANT	TITLE	PATENT NO.	ISSUED DATE
10700	10/323,008	12/18/02	Dinesh Maheshwari et al.	CHIRPED OPTICAL MEM DEVICE		
10800	10/159,330	5/30/02	Jahja Trisnadi et al.	DIFFRACTIVE LIGHT MODULATOR-BASED DYNAMIC EQUALIZER WITH INTEGRATED SPECTRAL MONITOR (as amended)		
10900	10/375,422	2/26/03	Robert Corrigan	METHOD AND APPARATUS FOR DYNAMIC WAVELENGTH BAND SHAPING		
11000	10/378,027	2/28/03	Robert Corrigan et al.	TWO-STAGE GAIN EQUALIZER		
11100	10/353,694	1/28/03	Jahja Trisnadi et al.	A MEMS INTERFEROMETER-BASED CONFIGURABLE OPTICAL ADD-AND-DROP MULTIPLEXER		
11200	10/186,911	6/28/02	Jim Hunter et al.	MICRO-SUPPORT STRUCTURES		
11200WO	PCT/US03/15 217	5/14/03	Silicon Light Machines	MICRO-SUPPORT STRUCTURES		
11500	10/377,840	2/28/03	Dinesh Maheshwari et al.	PDL MITIGATION STRUCTURE FOR DIFFRACTIVE MEMS AND GRATINGS		
11600	10/365,226	2/11/03	Charles B. Roxlo	WAVELENGTH SIGNAL SWITCH AND EQUALIZER		
11700	10/378,710	3/3/03	Chris Gudeman et al.	PRE-DEFLECTED BIAS RIBBONS		
11800	10/377,912	2/28/03	Michael Dueweke et al.	PATTERNED GLV RIBBON FOR PDL REDUCTION		
11900	10/377,936	2/28/03	Dinesh Maheshwari	DIFFRACTIVE LIGHT MODULATOR WITH DYNAMICALLY ROTATABLE DIFFRACTION PLANE		
12000	10/327,762	12/23/02	Dinesh Maheshwari	TILT-ABLE GRATING PLANE FOR IMPROVED CONTRAST IN 1XN STEPPED BLAZE SWITCHES		
12100	10/377,680	2/28/03	James Hunter et al.	MEM MICRO-STRUCTURES AND METHODS OF MAKING THE SAME		

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12200	10/377,994	2/28/03	James Hunter et al.	SILICON SUBSTRATE AS A LIGHT MODULATOR SACRIFICIAL LAYER		
12300	10/357,620	2/3/03	Clinton Carlisle et al.	A RECONFIGURABLE MODULATOR-BASED OPTICAL ADD-AND-DROP MULTIPLEXER		
12400	10/322,202	12/17/02	Dinesh Maheshwari	HIGH CONTRAST TILTING RIBBON BLAZED GRATING		
12500	10/322,213	12/17/02	Dinesh Maheshwari	ARBITRARY PHASE PROFILE FOR BETTER EQUALIZATION IN DYNAMIC GAIN EQUALIZER		